

Tracking Status and Stereo Studies

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Tracking Meeting

Oct 15, 2003

Tracking Status

5.1.0 Status

- Release for farms use: MC untested
- Done: Reprocessing of data started
 - Problem found with concatenation and first data processed is junk

5.2.0 Status

- Release for user analysis, simulation and production
- Release made and in testing
- Will test MC in 10K test
- Not enough man power to test Production - will be done eventually

5.3.0 Plan

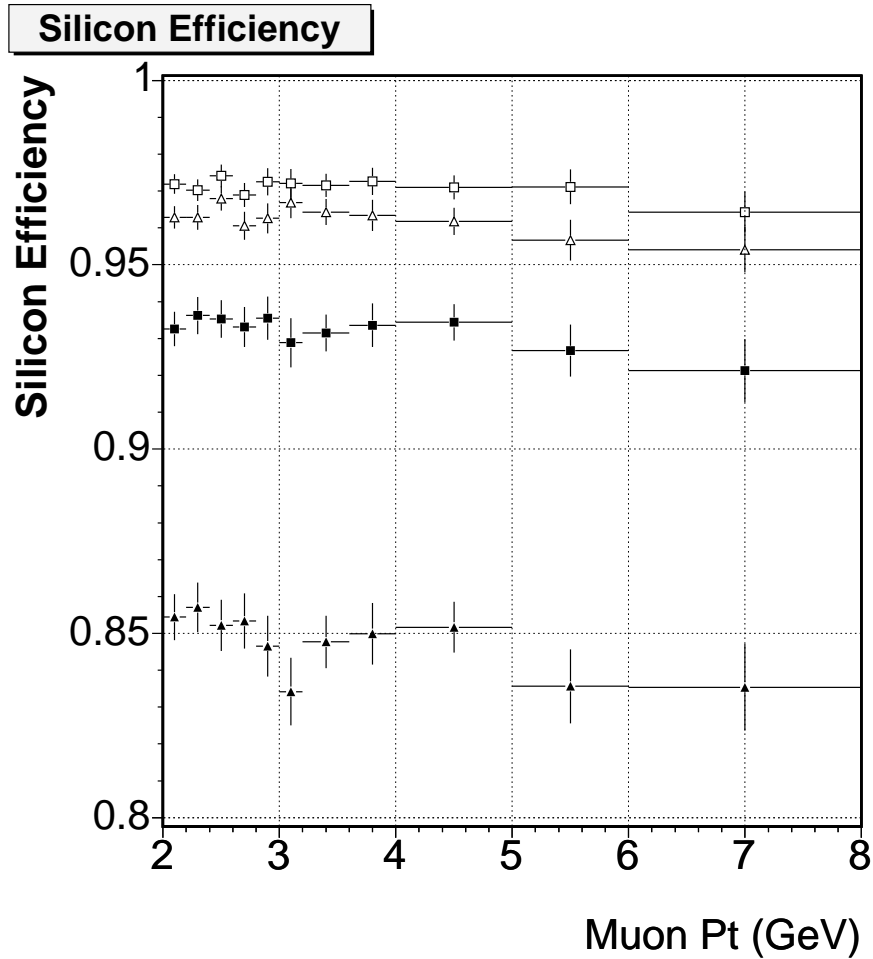
- For tuning of simulation: svx material and calorimeter response
- Also final version L00 code for winter analysis

Silicon Stereo OI Tracking Study

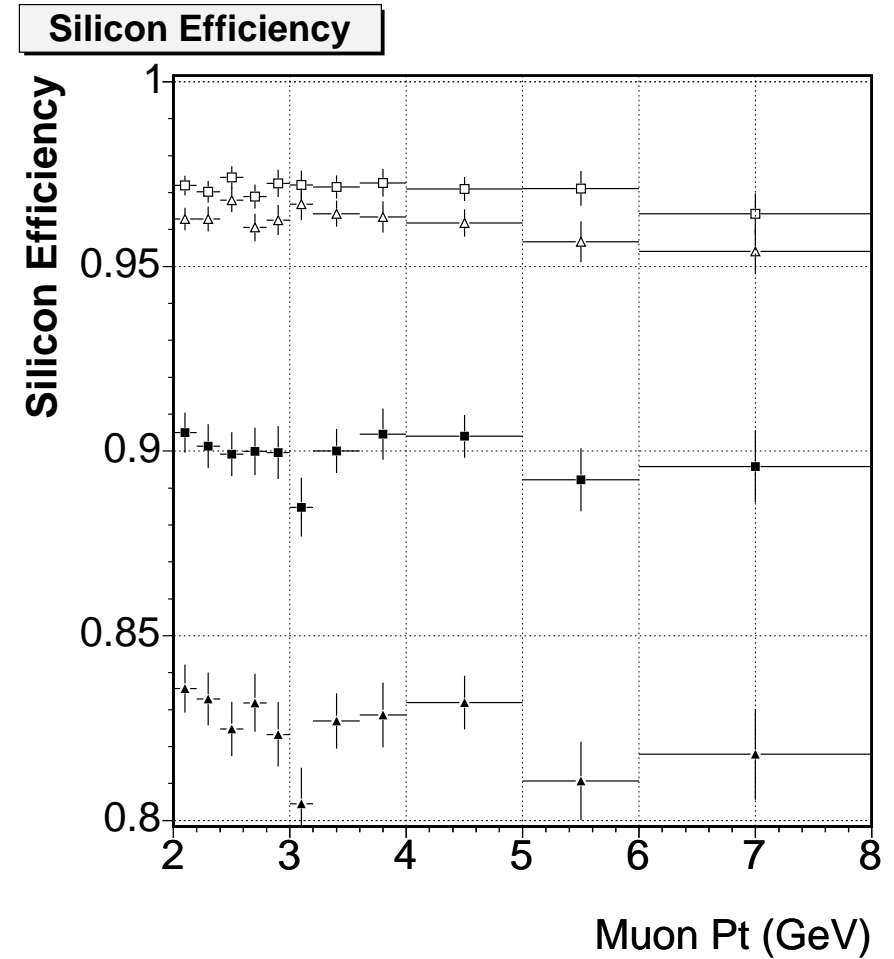
Study of efficiency and fake rate of stereo tracking

- Groundwork
 - Creating of new ChipOn table and integration into SiExpected - thanks Reid and Jason
 - Provides list of dead stereo ladders sides
 - Note only two stereo sides are partially dead(this information also available
- Study
 - Evaluate stereo efficiency
 - * Define stereo efficiency: 3 layers hit in SVXII or both SAS layers hit
 - * Measure efficiency using J/Psi COT track based denominator
 - * Background subtraction performed
 - Fake rate
 - * Pursue idea that elimination of tracks where stereo tracking failed may eliminate some axial fakes
- Next steps
 - Evaluate stereo performance
 - Resolution and stereo fake rate
 - Jason Galyardt working on this

Silicon Tracking Performance: J/ψ

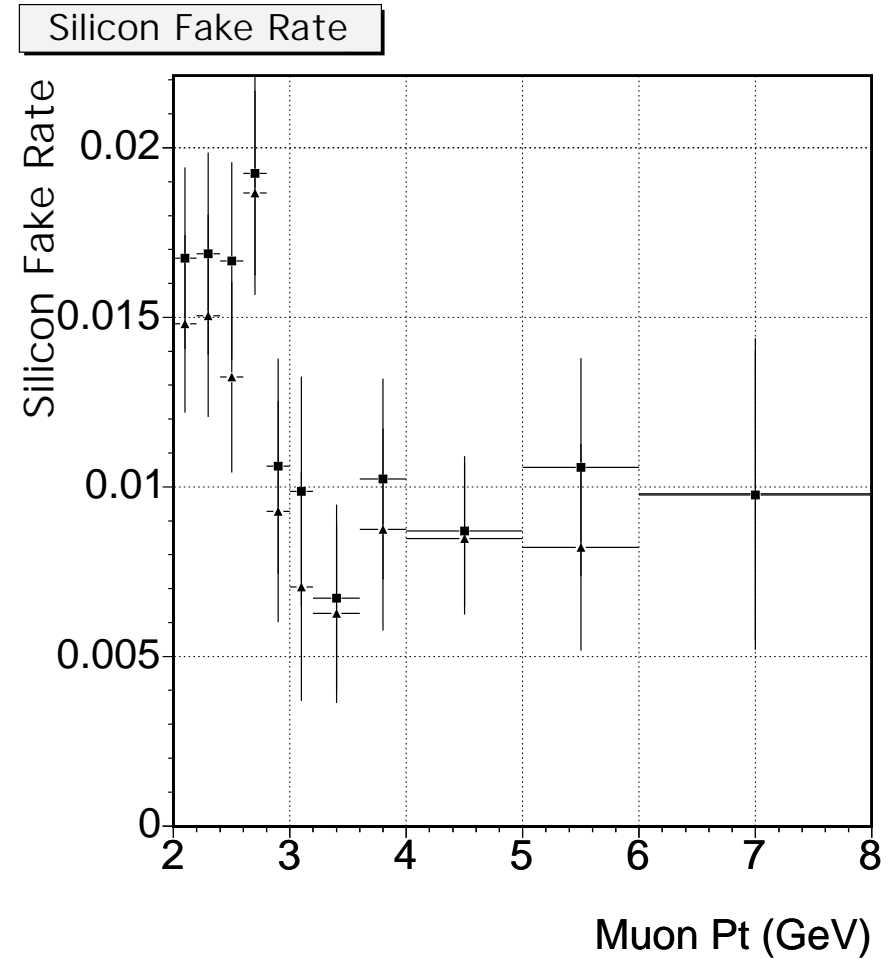
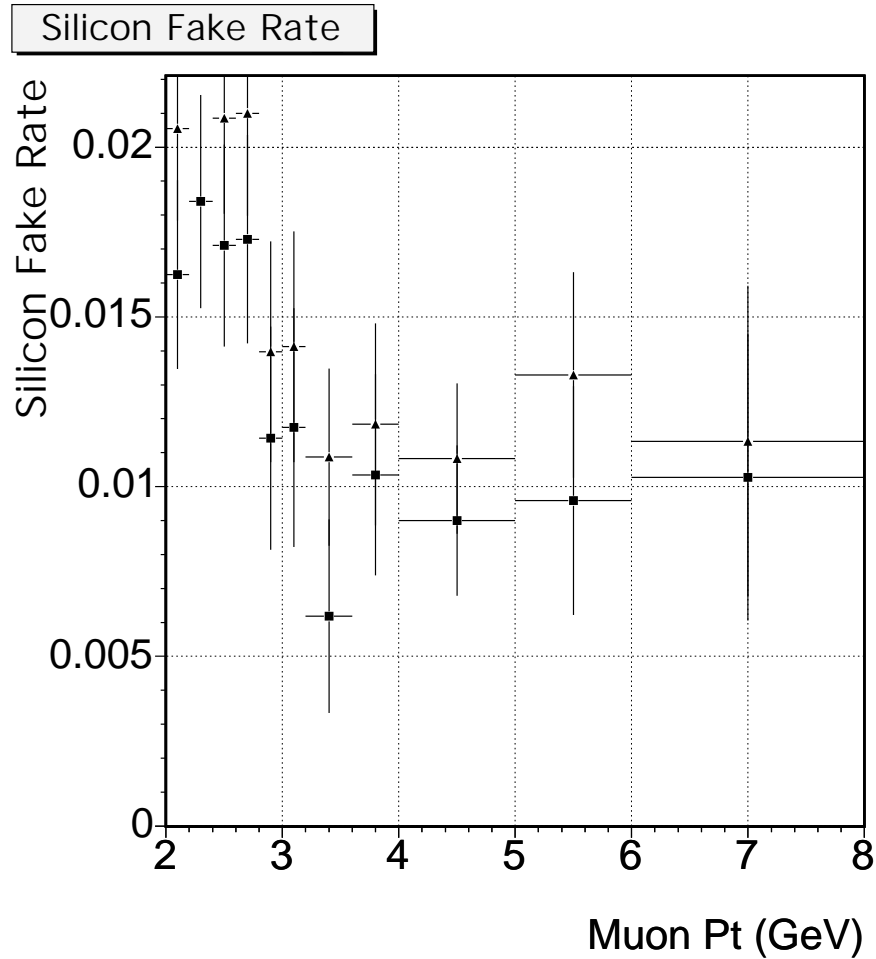


Stereo Eff: 84.8%, Acceptance: 96.3%



Apply tighter axial criteria: 82.7%

Silicon Tracking Performance: J/ψ



Stereo requirement slightly lowers fake rate for tighter requirements